**Q1.**

A single accumulator microprocessor supports the assembly language instructions:

LOAD       memory reference
ADD        memory reference
STORE      memory reference

An example instruction is:

LOAD 4

which would copy the contents of the referenced memory location 4 into the accumulator register.

(a)     (i)     Identify which part of the instruction is the *operand* and which part is the *opcode* by writing the words operand and opcode in the two boxes below.



**(1)**

(ii)     The accumulator is a general purpose register.

What is a *register*?

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**(1)**

(b)     Using the given assembly language instructions, write an assembly language program that adds together the values stored in memory locations 12 and 13, storing the resulting total in memory location 14.

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**(3)**

**(Total 5 marks)**

**Q2.**

**Figures 1, 2 and 3** show three versions of the same program.

|  |  |  |
| --- | --- | --- |
| **Figure 1** | **Figure 2** | **Figure 3** |
| Total := 60 + 10; | LD    #60 | 00101000    00111100 |
|   | ADD   #10 | 00111000    00001010 |
|   | ST   Total | 01100000    00101001 |

(a)     What type of translator program would be used to convert **Figure 1’s** program into **Figure 3’s** program?

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**(1)**

(b)     What type of translator program would be used to convert **Figure 2’s** program into **Figure 3’s** program?

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**(1)**

**(Total 2 marks)**

**Q3.**

The diagram below shows program code developed using different generations of programming languages.

|  |
| --- |
| **Program 1 (with comments)** |
| //CalculateFirstVar := 47;SecondVar := FirstVar + 2;FourthVar := ThirdVar; |

|  |
| --- |
| **Program 2 (with comments)** |
| AB2F ; Load value 2F into accumulatorBC5D ; Store contents of accumulator at address 5DE402 ; Add value 2 to accumulatorBCFF ; Store contents of accumulator at address FFAC61 ; Load accumulator with contents of address 61BC4A ; Store contents of accumulator at address 4A |

(a)     What generation of programming language was used to write **Program 1**?

**Don’t worry about this one as haven’t covered language generations**

**(1)**

(b)     Machine code can be represented in different numeric formats.

(i)     Which numeric format is used by the machine code program in **Program 2**?

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**(1)**

(ii)     State **one** reason for using this format.

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**(1)**

(iii)     The machine for which **Program 2** has been written has limited addressing capability.

What are the lowest and highest memory addresses that can be addressed by this machine?

Lowest address: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

Highest address: \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

**(1)**

(c)     Give an example of a situation for which it would be appropriate to write a program in a low level language (ie machine code or assembly language).

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**(1)**

(d)     Explain the differences between a compiler and an interpreter

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**(4)**

**(Total 9 marks)**