## **Objectives of this tutorial**

At the end of this lab you should be able to:

- Enter CPU instructions using the CPU simulator
- Describe the effect of compare instruction on CPU status flags Z and N
- Construct a loop and explain jump instructions that use Z and N flags
- Use direct addressing to access memory location(s)
- Use indirect direct addressing to access memory location(s)
- Construct a subroutine and call it
- Pass parameter(s) to the subroutine

## **Special instructions**

- Save your work at regular intervals.
- The CPU simulator is work in progress so it may crash from time to time. If this happens then restart it and load your latest code, hence the importance of the above statement!
- Ask if you need help with any aspect of this tutorial.
- The simulator is there to help you understand the theory covered in lectures so make the most of it.

## Exercises

Learning objectives: To enter CPU instructions and describe effect of compare instruction on CPU status flags Z and N.

In theory	In practice
The CPU instruction sets are often grouped together into categories containing instructions with related functions.	<ol> <li>Enter the following code and run it:</li> <li>MOV #1, R01 MOV #2, R02 ADD R01, R02</li> </ol>
This exercise familiarises you with the way the instructions are entered using the simulator.	<ul> <li>Add the following code and note the states of the status flags Z and N after each compare instruction is executed:</li> <li>CMP #3, R02 Z N</li> </ul>
The most encountered instructions involve data movements, e.g. the <b>MOV</b> instruction.	CMP #1, R02ZNCMP #4, R02ZNExplain your observations of the states of the status
Another common instruction is the comparison instruction, e.g. <b>CMP</b> . This instruction sets or resets the status flags <b>Z</b> and <b>N</b> as a record of the result of the comparison	flags after the compare instructions above:
operation.	** Now save the above code! **

Learning objectives: To construct a loop and explain jump instructions that use Z and N flags

In theory	In practice
The computer programs often contain loops where the same sequence of code is repeatedly executed until or while certain condition is met. Loops (or iterative statements) are the most useful features of programming languages. At the instruction level, loops use conditional jump instructions to jump back to the start of the loop or to jump out of the loop.	<ul> <li>3. Add the following code and run it (<u>ask your tutor how to enter a label</u>):</li> <li>MOV #0, R01 <ul> <li>Label1</li> <li>ADD #1, R01</li> <li>CMP #3, R01</li> <li>JLT \$Label1</li> </ul> </li> <li>Summarize what the above code is doing in plain English:</li> </ul>
This exercise is created to demonstrate the use of the jump instruction which often uses the result of a compare instruction. Such jump instructions use the <b>Z</b> and the <b>N</b> status flags to jump or not jump.	<ul> <li>4. Modify the above code such that R01 is incremented by 1 until it reaches the value of 4. Copy the code below:</li> <li>** Now save the above codel **</li> </ul>
	** Now save the above code! **

Learning objectives: To use direct addressing to access memory location(s)

In theory	In practice
Although instructions moving data to or from registers are the fastest instructions, it is still necessary to move data in or out of the main memory (RAM) which is a much	<ol> <li>5. Add the following code and run:</li> <li>STB #h41, 16</li> <li>LDB 16, R03</li> <li>ADD #1, R03</li> <li>STB R03, 17</li> </ol>
slower process. Examples of instructions used to store into or get data out of memory are explored here. The method used here uses the <u>direct addressing</u> method, i.e. the memory address is directly specified in the instruction	Make a note of what you see in the program's data area: What is the significance of <b>h</b> in <b>h41</b> above? Modify the above code to store the next two characters in the alphabet. Write down the modified code below:
itself.	

## \*\* Now save the above code! \*\*

Learning objective: To use indirect addressing to access memory location(s)

In theory	In practice
There are circumstances which make direct addressing unsuitable and inflexible method to use. In these cases <u>indirect addressing</u> is a more suitable and flexible method to use. In indirect addressing, the address of the memory location is not directly included in the instruction but is stored in a register which is the used in the instruction. This exercise introduces an indirect method of accessing memory. This is called register indirect addressing. There is also the memory indirect addressing but this is left as an exercise for you.	<ul> <li>6. Add the following code and run:</li> <li>Label2 MOV #16, R03 MOV #h41, R04 Label3 STB R04, @R03 ADD #1, R03 ADD #1, R04 CMP #h4F, R04 JNE \$Label3 Make a note of what you see in the program's data area:</li> <li>Explain the significance of @ in @R03 above:</li> </ul>
	** Now save the above code! **

Learning objective: To construct a subroutine and call it

In theory	In practice
Another very useful feature of	7. Add the following code but do <b>NOT</b> run it yet:
programming languages is subroutines. These contain sequences of instructions which can be executed many times. If a subroutine was not available the same sequence of instructions would have been repeated many times increasing the code size.	MSF CAL \$Label2 Now convert the code in (6) above into a subroutine by inserting a RET as the last instruction in the subroutine. Make a note of the contents of the PROGRAM STACK after the instruction MSF is executed (see tutor what to do to facilitate this observation):
At instruction level, a subroutine is called by an instruction such as <b>CAL</b> . This effectively is a jump instruction	Make a note of the contents of the <b>PROGRAM STACK</b> after the instruction <b>CAL</b> is executed:
but it also saves the address of the next instruction. This is later used by a subroutine return instruction, e.g. <b>RET</b> to return to the previous	What is the significance of the additional information?
sequence of instructions.	** Now save the above code! **

Learning objective: To pass parameter(s) to the subroutine

In theory	In practice
Useful as they are the subroutines are not very flexible without the use of the subroutine parameters. These are values passed to the subroutine to be consumed inside the subroutine. The common method of passing parameters to the subroutines is using the program stack. The parameters are pushed on the stack before the call instruction. These are then popped off the stack inside the subroutine and consumed. This exercise is created to	<ul> <li>8. Let's make the above subroutine a little more flexible. Suppose we wish to change the number of characters stored when calling the subroutine. Modify the calling code in (7) as below:</li> <li>MSF PSH #h60 CAL \$Label2 Now modify the subroutine code in (6) as below and run the above calling code: <ul> <li>Label2</li> <li>MOV #16, R03</li> <li>MOV #h41, R04</li> <li>POP R05 </li> <li>Label3</li> <li>STB R04, @R03</li> <li>ADD #1, R03</li> <li>ADD #1, R04</li> <li>CMP R05, R04 </li> <li>JNE \$Label3</li> <li>RET</li> </ul> </li> </ul>
demonstrate this mechanism.	Add a second parameter to change the starting address of the data as a challenge and write the code in the box above!

\*\* Now save the above code! \*\*