

Final



**General Certificate of Education (A-level)
January 2013**

Computing

COMP2

(Specification 2510)

**Unit 2: Computer Components, The Stored
Program Concept and The Internet**

Final

Mark Scheme

Mark schemes are prepared by the Principal Examiner and considered, together with the relevant questions, by a panel of subject teachers. This mark scheme includes any amendments made at the standardisation events which all examiners participate in and is the scheme which was used by them in this examination. The standardisation process ensures that the mark scheme covers the candidates' responses to questions and that every examiner understands and applies it in the same correct way. As preparation for standardisation each examiner analyses a number of candidates' scripts: alternative answers not already covered by the mark scheme are discussed and legislated for. If, after the standardisation process, examiners encounter unusual answers which have not been raised they are required to refer these to the Principal Examiner.

It must be stressed that a mark scheme is a working document, in many cases further developed and expanded on the basis of candidates' reactions to a particular paper. Assumptions about future mark schemes on the basis of one year's document should be avoided; whilst the guiding principles of assessment remain constant, details will change, depending on the content of a particular examination paper.

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COMPONENT NUMBER: COMP2

COMPONENT NAME: COMPUTER COMPONENTS, STORED PROGRAM CONCEPT AND THE INTERNET

1. This mark scheme contains the correct responses which we believe that candidates are most likely to give. Other valid responses are possible to some questions and should be credited. Examiners should refer off mark scheme responses that they believe are creditworthy to a Team Leader.

The following annotation is used in the mark scheme:

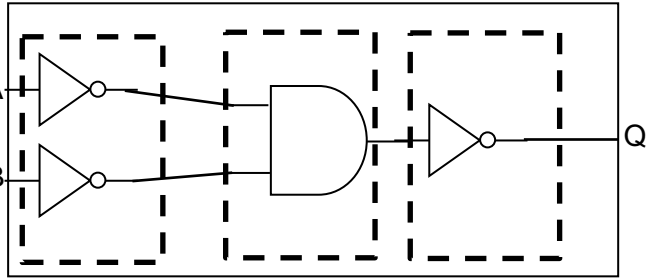
- ;** - means a single mark
- //** - means alternative response
- /** - means an alternative word or sub-phrase
- A** - means acceptable creditworthy answer
- R** - means reject answer as not creditworthy
- NE** - means not enough
- I** - means ignore
- DPT** - means "Don't penalise twice". In some questions a specific error made by a candidate, if repeated, could result in the loss of more than one mark. The **DPT** label indicates that this mistake should only result in a candidate losing one mark, on the first occasion that the error is made. Provided that the answer remains understandable, subsequent marks should be awarded as if the error was not being repeated.

Qu	Part	Sub-part	Marking Guidance	Marks	Comments
1			<p>Hardware: physical components // devices (that make up the computer) // electrical circuit // electrical components (that make up the computer);</p> <p>NE components</p> <p>Software: programs/instructions that are run/executed (by the computer);</p> <p>A. codes that are run/executed A. programs that enables computer to run A. programs that allow user to perform tasks</p>	2	Need both parts to gain mark

2	a	<p>A set of/group of/parallel wires/lines; that are used to connect together components (inside the computer) // connect different parts of the CPU; in order to pass signals between them;</p> <p>R a wire A. connect different parts of the computer NE data</p>	<p>MAX 2</p>	<p>Wires needs to be qualified with set/group</p>
2	b	<p>Instructions;</p> <p>A. Commands / machine-code R signals</p> <p>Examples of a control signal (max 1):</p> <p>Clock/timing; reset; interrupt ACK; interrupt request; bus grant; bus request; status; I/O write; I/O read; memory read; memory write; transfer ACK</p> <p>A. interrupt A. transfer request A. read/write NE load /store NE clock speed</p>	<p>2</p>	<p>NE an event that details when an interrupt would be caused</p>
2	c	<div data-bbox="454 1211 1077 1608" data-label="Diagram"> <p>The diagram illustrates a computer system with four main components: Processor, Main Memory, Keyboard controller, and Graphics Controller. These components are connected to three system buses: Address bus, Control bus, and Data bus. A Clock signal is also connected to the Processor. The Address bus is shown at the top, with arrows indicating bidirectional communication between the Processor and the other three components. The Control bus is shown in the middle, with arrows indicating bidirectional communication between the Processor and the other three components. The Data bus is shown at the bottom, with arrows indicating bidirectional communication between the Processor and the other three components. A dashed box encloses the Keyboard controller and Graphics Controller components.</p> </div> <p>1 mark – one of processor, keyboard controller or graphics controller identified correctly 2 marks – all three correctly identified</p> <p>Address bus connects the 4 components; Arrow from processor to the address bus; Arrows from address bus to the three other components;</p>	<p>5</p>	<p>Mark this on where the candidate has put the components.</p>

3		<p>General:</p> <p>Idea of 'quicker to write' or 'easier to write' [ONE MARK] EXAMPLES: Assembly language is quicker to write than machine code // HLL is quicker to write (compared to assembly code) // Assembly language is easier to write than machine code // HLL is easier to write (compared to assembly); [or opposites – slower to write / harder to write]</p> <p>Idea of 'understanding' [ONE MARK] EXAMPLES: Assembly code easier to understand than machine code // HLL easier to understand than assembly code;</p> <p>Idea of 'debugging' [ONE MARK] EXAMPLES: Assembly code easier to debug than machine code // HLL easier to debug (than assembly code);</p> <p>Assembly language: Solution expressed in terms of mnemonics; A. an example of a full instruction (operand and opcode) Easier to make mistakes in assembly language; Instruction composed of op-code and operand; Solution translated by using an assembler; Code is hard to port to other types of computer // machine-oriented languages; One assembly language instruction relates to one machine code instruction; Situation – working on embedded hardware // need for small object code size // need for fast execution // need to access hardware/registers directly;</p> <p>Imperative language: Imperative is where the programmer gives the computer a sequence of instructions to perform; Selection/Sequence/Iteration constructs available; A. a full example of a selection/iteration construct Library of pre-written functions available; Solution translated by using a compiler / interpreter; A compiler might not be available for a specific processor (disadvantage); Situation – anything sensible that would need a</p>	MAX 8	
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		<p>HLL (for example games programming)</p> <p>Declarative language: (Certain languages) define what is to be computed rather than how the computation is to be done; (Certain languages) lack side effects; (Certain languages) have a clear link to mathematical logic; (Certain languages) express solutions in terms of facts and rules // rule-based; (Certain languages) will use an inference engine to work out the answer; The user asks a question of the system rather than provide an algorithm of the solution; Uses back-chaining/backtracking; (Certain languages) express solutions using markup languages (such as HTML); (Certain languages) express solutions as CSS / regular expressions / (subset of) SQL; A. example code from part of a declarative program (ie an SQL statement) Situation – medical diagnosis // expert systems // database query //creating a web page/website ;</p> <p>Imperative and Declarative language: Solution expressed in terms of statements written using <u>English-like keywords</u>; Code easier than assembly language to port to other types of computer; One language statement maps to many (more than one) machine code instruction;</p> <p>NOTE: accept any sensible situation for each area.</p> <table border="1" data-bbox="438 1523 1082 2067"> <thead> <tr> <th colspan="2">Mark Bands and Description</th> </tr> </thead> <tbody> <tr> <td style="vertical-align: top;">7-8</td> <td> <p><i>To achieve a mark in this band, candidates must meet the subject criterion (SUB) and all 5 of the quality of language criteria (QWCx).</i></p> <p>SUB Candidate has covered all three language generations and made at least 7 subject-related points.</p> <p>QWC1 Text is legible.</p> <p>QWC2 There are few, if any, errors of spelling, punctuation and grammar. Meaning is clear.</p> <p>QWC3 The candidate has selected and used a form and style of writing appropriate to the purpose and has expressed ideas clearly and fluently.</p> <p>QWC4 Sentences (and paragraphs) follow on from one another clearly and coherently.</p> </td> </tr> </tbody> </table>	Mark Bands and Description		7-8	<p><i>To achieve a mark in this band, candidates must meet the subject criterion (SUB) and all 5 of the quality of language criteria (QWCx).</i></p> <p>SUB Candidate has covered all three language generations and made at least 7 subject-related points.</p> <p>QWC1 Text is legible.</p> <p>QWC2 There are few, if any, errors of spelling, punctuation and grammar. Meaning is clear.</p> <p>QWC3 The candidate has selected and used a form and style of writing appropriate to the purpose and has expressed ideas clearly and fluently.</p> <p>QWC4 Sentences (and paragraphs) follow on from one another clearly and coherently.</p>		
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4	a			3
			 <p>The diagram shows a logic circuit. On the left, there are two input lines labeled 'A' and 'B'. Each input line passes through a NOT gate (represented by a triangle with a small circle at its tip). The outputs of these two NOT gates are connected to the two inputs of an AND gate (represented by a D-shaped symbol). The output of the AND gate is connected to the input of a third NOT gate. The output of this final NOT gate is connected to an output line labeled 'Q'. A dashed rectangular box encloses the two NOT gates at the beginning and the AND gate in the middle.</p>	
			<p>1 mark – logic of first part satisfies NOT A, NOT B;</p> <p>1 mark – inputs into an AND gate;</p> <p>1 mark – output from AND gate passes through a NOT gate and connected to Q;</p>	

4	b		<table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>A</td><td>B</td><td>A + B</td></tr> <tr><td>0</td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>1</td></tr> </table> <p>1 mark for correct A + B column;</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <tr><td>A</td><td>B</td><td>\bar{A}</td><td>\bar{B}</td><td>$\bar{A} \cdot \bar{B}$</td><td>$\overline{\bar{A} \cdot \bar{B}}$</td></tr> <tr><td>0</td><td>0</td><td>1</td><td>1</td><td>1</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td><td>0</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>0</td><td>1</td><td>0</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>0</td><td>0</td><td>0</td><td>1</td></tr> </table> <p>1 mark for columns \bar{A} and \bar{B} being correct; 1 mark for $\bar{A} \cdot \bar{B}$ column being correct; 1 mark for $\overline{\bar{A} \cdot \bar{B}}$ column being correct;</p> <p>NOTE: Can follow through into $\bar{A} \cdot \bar{B}$ column from previous two</p>	A	B	A + B	0	0	0	0	1	1	1	0	1	1	1	1	A	B	\bar{A}	\bar{B}	$\bar{A} \cdot \bar{B}$	$\overline{\bar{A} \cdot \bar{B}}$	0	0	1	1	1	0	0	1	1	0	0	1	1	0	0	1	0	1	1	1	0	0	0	1	4	
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4	c		De Morgan's (law);	1																																														
4	d		<p>Mark allocation:</p> <p>One mark for taking either A, NOT C or A AND NOT C outside of brackets to produce a correct expression; One mark for eliminating B in a valid way; One mark for correct final answer;</p> <p>Example One: $A \cdot B \cdot \bar{C} + A \cdot \bar{C}$</p> <p>$A (B \cdot \bar{C} + \bar{C})$ - taking A outside of brackets;</p> <p>$A (\bar{C} (B + 1))$ $(B + 1) = 1$ Simplifying to remove B using $B + 1 = 1$;</p> <p>$B \cdot \bar{C} + \bar{C} = \bar{C}$ Simplifying to remove B using $B \cdot \bar{C} + \bar{C} = \bar{C}$;</p> <p>A. $A (\bar{C} (B + 1)) \rightarrow A \cdot \bar{C}$;</p> <p>Final answer $A \cdot \bar{C}$</p>	3																																														

		<p>Example Two:</p> <p>$A.B.\bar{C} + A.\bar{C}$</p> <p>$A.\bar{C}(B + 1)$ – taking outside of brackets; $(B + 1) = 1$; - simplifying to remove B</p> <p>A. $A.\bar{C}(B + 1) \rightarrow A.\bar{C}$</p> <p>Final answer $A.\bar{C}$</p> <p>Truth Table Method</p> <table border="1" data-bbox="438 663 1043 954"> <thead> <tr> <th>A</th> <th>B</th> <th>C</th> <th></th> <th>$A.\bar{C}$</th> <th>$A.B.\bar{C} + A.\bar{C}$</th> </tr> </thead> <tbody> <tr><td>0</td><td>0</td><td>0</td><td></td><td>0</td><td>0</td></tr> <tr><td>0</td><td>0</td><td>1</td><td></td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>0</td><td></td><td>0</td><td>0</td></tr> <tr><td>0</td><td>1</td><td>1</td><td></td><td>0</td><td>0</td></tr> <tr><td>1</td><td>0</td><td>0</td><td></td><td>1</td><td>1</td></tr> <tr><td>1</td><td>0</td><td>1</td><td></td><td>0</td><td>0</td></tr> <tr><td>1</td><td>1</td><td>0</td><td></td><td>1</td><td>1</td></tr> <tr><td>1</td><td>1</td><td>1</td><td></td><td>0</td><td>0</td></tr> </tbody> </table> <p>(student answer may have more columns than this)</p> <p>A mark for having correct column for $A.B.\bar{C} + A.\bar{C}$; A mark for having correct column for $A.\bar{C}$;</p> <p>Final answer $A.\bar{C}$</p>	A	B	C		$A.\bar{C}$	$A.B.\bar{C} + A.\bar{C}$	0	0	0		0	0	0	0	1		0	0	0	1	0		0	0	0	1	1		0	0	1	0	0		1	1	1	0	1		0	0	1	1	0		1	1	1	1	1		0	0		
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5	a	<p>Magnetic (medium); Binary digits/bits/0s and 1s/data represented by magnetising spots on disk // changing magnetic properties of disk; Disk made up of platter(s); Disk divided into tracks and sectors; A. either tracks or sectors alone Tracks are concentric circles // organised into cylinders; Drive head can move in/out // moves to track/cylinder // moves radially; Disk continuously spinning (while in operation); Disk spins at high speed // feasible example of speed; Data read/written as correct sector passes under read/write head; A. drive head Data transferred in sectors/blocks; Medium and drive/device in sealed enclosure; Hard disk drive is a random access device; A. Head parked / not over disk when not in use // head must not touch surface when in use; A. Use of cache/buffer to speed up data transfer;</p> <p>MAX 3 if candidate talks about lasers / making holes / pins / engraving</p>	<p>MAX 4</p>																																																							

5	b	<p>512 MB x 2 = 1024 MB = 1GB 1GB x 1024 = 1 TB</p> <p>2 x 1024 = 2048</p> <p>Award mark for a clear movement between MB – GB - TB making use of 1024 ;</p> <p>Final answer: 2048;</p> <p>Acceptable alternative (as many hard drive manufacturers do not use the 1024 principle) :</p> <p>1 TB = 1000 GB = 1000000 MB;</p> <p>1000 000/512 = 1953.125; (mark to be awarded for understanding the calculation needed)</p> <p>Final answer : 1953.125;</p> <p>A. Accept a final answer that has involved some approximation as a no calculator paper. (2000;)</p> <p>Alternative</p> <p>$2^{40} / 2^{29} ; = 2^{11} ;$</p>	MAX 2	
5	c	<p>More platters (which are packed closer); Greater density of data on each platter; More tracks on a platter // more cylinders; Change to perpendicular magnetic domains; Ability to write smaller magnetic domains/parts // smaller read/write heads; Use of different alloy materials for the platters;</p>	MAX 1	
5	d	<p>Faster access speed // faster booting of operating system // faster data transfer/read/write speeds; Silent operation; Are lighter; Less heat generated; Less power required // longer battery life; Less susceptible to damage from physical shocks // more robust (due to no moving parts);</p> <p>NE quicker (without explanation) NE better performance (without explanation)</p>	MAX 2	<p>Accept – quicker as no need to wait for read/write head to move//sector to be underneath read/write head;</p>

6	a		<p>WWW (max 3 marks) A system of interlinked / hypertext documents; Accessed via the Internet; Using HTTP protocol;</p> <p>NE web a collection of web pages</p> <p>Internet (max 3 marks) A network of interconnected computer networks; A. a network of computers; Using a <u>globally</u> unique address space; Using end-to-end communication protocol // Internet Protocol // “TCP / IP”;</p> <p>Supports a range of application protocols; A. two examples of different protocols; R. “TCP” R. “IP”</p>	MAX 4	
6	b		<p>Messages split into packets; A. chunks Each packet given destination/source address; Each packet dispatched to the Internet through a router/gateway; Packets sent independently; Packets given a sequence number; Routers forward packets (until they reach destination); Path of packet transfer determined by router(s); Packets reassembled at the destination;</p>	MAX 2	
6	c		<p>12.23.45.89</p> <p>An IP (v4) address (that uniquely identifies a machine on the Internet) // Internet protocol address;</p> <p>80</p> <p>A port number // a number that specifies which process on the receiving machine/host to send the data to; A. port;</p> <p>Denotes that HTTP (server) is recipient of packet // packet is an HTTP packet</p>	2	

7	a		<div style="border: 1px solid black; padding: 5px;"> <p>Manor School Library X</p> <hr/> <p>Our favourite genres are:</p> <ul style="list-style-type: none"> • Science fiction • Suspense • Comedy <p><u>Discover our Top Ten Books</u></p> </div> <p>'Manor School Library' – in title bar;</p> <p>Line space after 'Our favourite genres are:' with the correct text AND line space after the bulleted list ;</p> <p>Use of un-ordered list with three bulleted points with correct text;</p> <p>Hyperlink identified through underlining or clear label with the correct text;</p> <p>A. minor spelling mistakes</p> <p>MAX 3: if any errors in drawing of page (for example font size differences or indenting hyperlink)</p>	4	
7	b	i	<p>The text inside the <p> tags/ paragraph will be blue and use the Arial font //</p> <p>The text 'Our favourite genres are:' will be blue and use the Arial font;</p>	1	Candidate needs to talk about text being blue
7	b	ii	<pre>#header{ font-size: 36pt; color: green; }</pre> <p>MAX 2 if ; separator missing between 36pt and color</p> <p>1 mark - #header {} // div {} // div#header{} ; [Not contents]</p> <p>1 mark – for color: green ;</p> <p>1 mark – for font-size: 36pt ;</p> <p>NOTE : color must be spelt without the u For green accept #00xx00 where xx in range 01 to FF</p>	3	

8	a		Copyright, Designs and Patents (Act); A. Digital Economy Act	1	
8	b		No money goes to the artists / publishers / distributors; The quantity/amount of music being produced could go down; (Pirated) music can be of a lower quality;	MAX 2	
8	c		Can sell on items that have been purchased; Can play on any suitable device // Can be played on many devices that the purchaser might own; Can make backup copies; Can play without any time limit// no limit on amount of plays; Can load into any suitable software package; Encourages creativity / sharing / remixing / reworking; A. user has full control over their music	MAX 2	

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